

**In the Specification**

Please amend the paragraph at page 6, line 27 – page 7, line 2 as follows:

Figure 1 shows a predistortion power amplifier circuit architecture incorporating a gain/phase comparator (100) and a predistorter (200) according to the present invention. An RF input signal (10) is applied to an input (19) of a high power amplifier (22) via a directional coupler (12), a first delay line (14), an amplitude modulator (16) and a phase modulator (18). The output (24) of the amplifier (22) provides an amplified output signal (28) which is sampled by a directional coupler (26). The sampled RF output (30) from the directional coupler (12) is applied to a power splitter (32), the outputs of which are connected to an envelope detector (34) and a second delay line (40). The output (36) of the envelope detector is connected to an adaptive pre-distorter subsystem (200). The adaptive pre-distorter subsystem (200) generates two outputs: a gain correction signal (92) which is connected to the control port of amplitude modulator (16); and a phase correction signal (94) which is connected to the control port of phase modulator (18).

Please amend the paragraph at page 7 lines 32-37 as follows:

The gain/phase comparator system (100) requires as inputs a sample (42) of the input signal (10) and a sample (54) of the output signal (28), normalized to the same signal level and aligned in time. Output sample (54) is normalised to the same level as (42) by attenuating the coupled output (50) of coupler (26) in attenuator (52); input sample (42) is time-aligned with (54) by delaying one output of power splitter (32) in delay line (40).